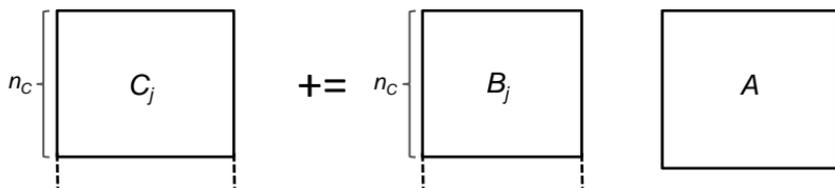
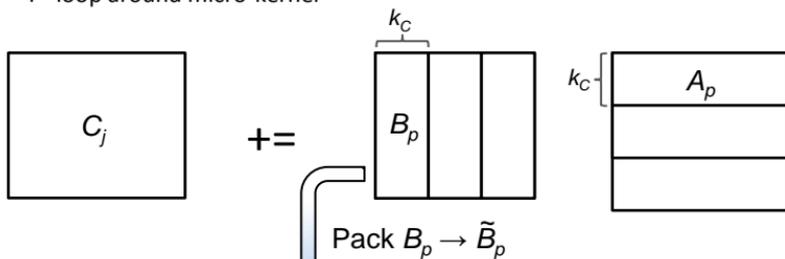


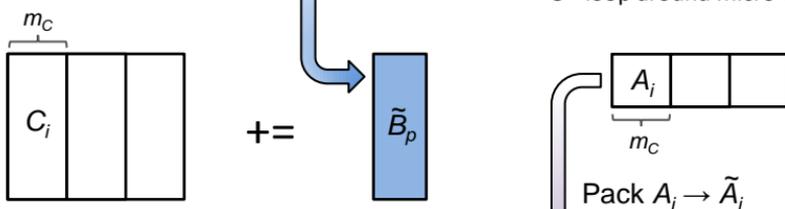
5th loop around micro-kernel



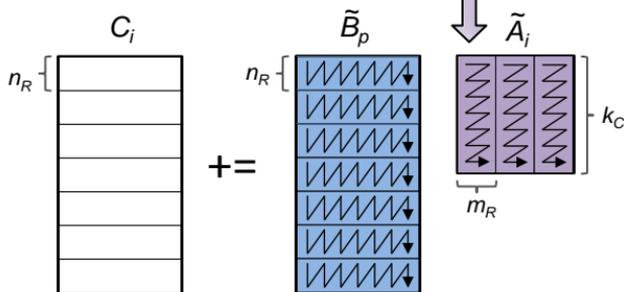
4th loop around micro-kernel



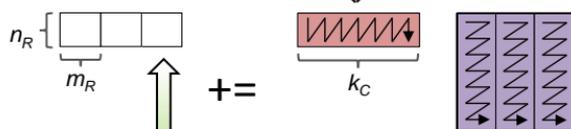
3rd loop around micro-kernel



2nd loop around micro-kernel



1st loop around micro-kernel



micro-kernel

- main memory
- L3 cache
- L2 cache
- L1 cache
- registers

